

Actel predstavil vývojový kit **SmartFusion A2F500** určený na prácu s čipom SmartFusion A2F500M3G-FGG484ES - FPGA s hw ARM jadrom Cortex-M3.

Kit obsahuje FLASH pamäť (8-MByte SPI flash Atmel AT25DF641), SRAM pamäť (2x 16-Mbit SRAM Cypress CY7C1061DV33), Ethernet 10/100, 2x CAN, RS485, I2C, USB, 9616-pixelový OLED displej a pár tlačítko.

Na kite nájdete



- A2F500M3G-FGG484ES
- 500,000 System FPGA gates, 512 KB flash memory, 64 KB SRAM, and additional distributed SRAM in the FPGA fabric and external memory controller
- Peripherals include Ethernet, DMAs, I2Cs, UARTs, timers, ADCs, DACs and additional analog resources
- Refer to the SmartFusion product page for full device information
- Flash Memory, SRAM Memory, and PSRAM
- EtherCAT ASIC

Rozhrania kitu

- Programming connection for program and debug from Actel's design tools
- RVI header for application programming and debug from either Keil or IAR Systems
- Mixed-signal header for daughter card support
- USB-to-UART connection to UART_0 for HyperTerminal examples
- DB9 connector for RS485 UART_1
- Two DB9 connectors for CAN ports
- RJ45 connector for 10/100 Ethernet (on-chip MAC and external PHY)
- Two RJ45 connectors for EtherCAT port connection through soft SPI core in the fabric and external PHY)
- IGLOO PLUS expansion header for I/O expansion (for routing through the EMC I/Os)
- A2F500 connector provides access to additional digital I/Os available in the A2F500 device
- I2C headers
- SPI headers
- DirectC header

Cena

za kit (A2F-EVAL-KIT) je 999,- USD

Odkazy

[Homepage](#)

[SmartFusion Development Kit User's Guide](#)

Distribúcia

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