

Ide o μ PD78F8056, μ PD78F8057, a μ PD78F8058. MCU sú v 56 pinovom QFN púzdre a líšia sa veľkosťou ROM pamäte. MCU sú ideálne pre aplikácie diaľkových ovládaní RF4CE.

Vlastnosti

- Power supply voltage: VDD = 1.8 to 3.6 V
 - Oscillation frequency: 20 MHz
 - ROM capacities: 64 KB to 128 KB Flash memory
 - RAM capacities: 8 KB
 - 56-pin plastic QFN package
 - 78K0R CPU core
 - On-chip 2.4 GHz RF transceiver
IEEE 802.15.4-2006 (modulation system: O-QPSK, spreading system: DSSS, data rate: 250 kbps)
 - On-chip internal high-speed oscillation clocks
 - 20 MHz Internal high-speed oscillation clock: 20 MHz TYP. - 8 MHz Internal high-speed oscillation clock: 8 MHz TYP. - 1 MHz Internal high-speed oscillation clock: 1 MHz TYP.
 - On-chip single-power-supply flash memory (with prohibition of chip erase/block erase/writing function)
 - Self-programming (with boot swap function/flash shield window function)
 - On-chip debug function
 - On-chip power-on-clear (POC) circuit and low-voltage detector
 - On-chip watchdog timer (operable with the dedicated internal low-speed oscillation clock)
 - On-chip multiplier/divider (16 bits x 16 bits, 32 bits/32 bits)
 - On-chip BCD adjustment
 - I/O ports: 18
 - Timer: 14 channels
 - 16-bit timer: 12 channels
 - Watchdog timer: 1 channel
 - Real-time counter: 1 channel
 - Serial interface
 - CSI: 1 channels (used exclusively for internal communication with the 2.4 GHz RF transceiver)
 - CSI: 1 channel/UART: 1 channel/simplified I2C: 1 channel
 - UART(Transmission only): 1 channel
 - UART(LIN-bus supported): 1 channel
- Operating ambient temperature: TA = -40 to +85 °C

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