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- Operating Power-Supply Voltage Range of 1.65 V to 3.6 V
- Supports QWERTY Keypad Operation Plus GPIO Expansion
- 18 GPIOs Can Be Configured into Eight Inputs and Ten Outputs to Support an 8 × 10 Keypad Array (80 Buttons)
- ESD Protection Exceeds JESD 22 on all 18 GPIO Pins and non GPIO pins
- 2000-V Human Body Model (A114-A)
- 1000-V Charged Device Model (C101)
- Low Standby (Idle) Current Consumption: 3 µA
- Polling Current Drain 70 µA for One Key Pressed
- 10 Byte FIFO to Store 10 Key Presses and Releases
- Supports 1-MHz Fast Mode Plus I²C Bus
- Open-Drain Active-Low Interrupt Output, Asserted when Key is Pressed and Key is Released
- Minimum Debounce Time of 50 μs
- Schmitt-Trigger Action Allows Slow Input Transition and Better Switching Noise Immunity at the SCL and SDA Inputs: Typical $V_{\rm hys}$ at 1.8 V is 0.18 V
- Latch-Up Performance Exceeds 200 mA Per JESD 78, Class II
- Very Small Packages
- WCSP (YFP): 2 mm \times 2 mm; 0.4 mm pitch
- QFN (RTW): 4 mm × 4 mm; 0.5 mm pitch

CAD_INT pin is included to indicate the detection of CTRL-ALT-DEL (i.e., 1, 11, 21) key press action (YFP package).

Datasheet Homepage