

Microchip predstavil, dvojkanálový *analog front end* s dvomi synchronne vzorkujúcimi sigma - delta A/D prevodníkmi, kompenzáciou fázového oneskorenia, integrovanou napäťovou referenciou, dvomi PGA (Programable Gain Amplifier) a 20MHz sériovým rozhraním SPI.

Vlastnosti

- Two synchronous sampling 16/24-bit resolution Delta-Sigma A/D Converters with proprietary multi-bit architecture
- 91 dB SINAD, -104 dBc THD (up to 35th harmonic), 109 dB SFDR for each channel
- Programmable data rate up to 64 ksps
- Ultra low power shutdown mode with <2 μ A
- -133 dB Crosstalk between the two channels
- Low Drift Internal Voltage Reference: 12 ppm/°C
- Differential Voltage Reference Input Pins
- High Gain PGA on each channel (up to 32 V/V)
- Phase Delay Compensation between the two channels with 1 μ s time resolution
- Separate Modulator outputs for each channel
- High-Speed Addressable 20 MHz SPI Interface with Mode 0,0 and 1,1 Compatibility
- Independent analog and digital power supplies 4.5V-5.5V AVDD, 2.7V-5.5V DVDD
- Low Power consumption (14 mW typical at 5V)
- Available in small 20-lead QFN and SSOP packages
- Industrial Temperature Range -40°C to +85°C

Oblasti využitia

- Energy Metering & Power Measurement
- Automotive
- Portable Instrumentation
- Medical and Power Monitoring

[datasheet](#)