

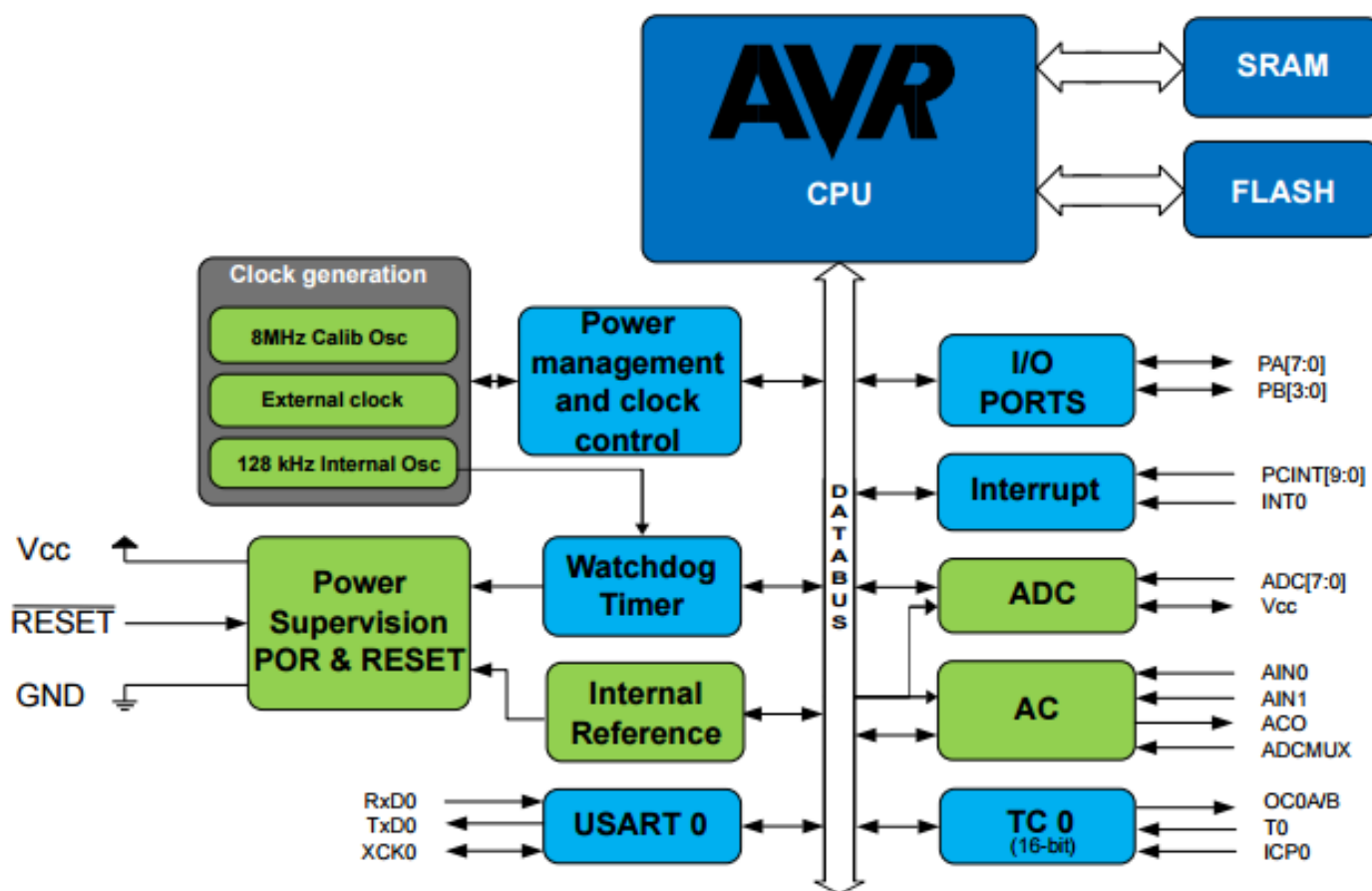
Atmel uviedol nových členov nízkopríkonovej 8-bitovej ATtiny rodiny - ATtiny102/ATtiny104.

Vlastnosti

- Advanced RISC Architecture
 - 54 Powerful Instructions
 - Mostly Single Clock Cycle Execution
 - 16 x 8 General Purpose Working Registers
 - Fully Static Operation
 - Up to 12 MIPS Throughput at 12MHz
- Non-volatile Program and Data Memories
 - 1024 Bytes of In-system Programmable Flash Program Memory
 - 32 Bytes Internal SRAM
 - Flash Write/Erase Cycles: 10,000
 - Data Retention: 20 Years at 85°C / 100 Years at 25°C
 - Self-programming Flash on Full Operating Voltage Range (1.8 – 5.5V)
- Peripheral Features
 - One 16-bit Timer/Counter (TC) with Prescaler, Input Capture, Two Output Capture and Two PWM Channels
 - Programmable Watchdog Timer (WDT) with Separate On-chip Oscillator
 - Selectable Internal Voltage References: 1.1V, 2.2V and 4.3V
 - 10-bit ADC with 8-channels/14-pin and 5-channel/8-pin Package Options
 - On-chip Analog Comparator (AC)
 - Serial Communication Module: Single USART
- Special Microcontroller Features
 - In-system Programmable
 - External Programming (2.7 – 5.5V)
 - Self Programming (1.8 – 5.5V)
 - External and Internal Interrupt Sources
 - Low Power Idle, ADC Noise Reduction, and Power-pown Modes
 - Enhanced Power-on Reset Circuit
 - Programmable Supply Voltage Level Monitor with Interrupt and Reset
 - Accurate Internal Calibrated Oscillator
 - Fast and Normal Start-up Time Options Available
- I/O and Packages
 - 12 Programmable I/O Lines for ATtiny104 and 6 Programmable I/O Lines for ATtiny102
 - 8-pin UDFN (ATtiny102)

- 8-pin SOIC150 (ATTiny102)
- 14-pin SOIC150 (ATTiny104)
- Operating Voltage: 1.8 - 5.5V
- Temperature Range: Up to -40 +125°C
- Speed Grades
 - 0 - 4MHz at 1.8 - 5.5V
 - 0 - 8MHz at 2.7 - 5.5V
 - 0 - 12MHz at 4.5 - 5.5V

Blokové zapojenie



Cena, dostupnosť

Vzorky by mali byť už dostupné a plná produkcia je naplánovaná na 05/2016.

Cena podľa Mouseru je:

[ATTINY102-SSNR](#) - od 0.482 Eur @4k pcs

[ATTINY104-SSNR](#) - od 0.532 Eur @4k pcs

Odkazy

[Datasheet](#)

Distribúcia

[vid'. Adresár](#)